

Notice of References CitedApplication/Control No.
09/964,515Applicant(s)/Patent Under
Reexamination
KIMURA ET AL.Examiner
A. M. ThompsonArt Unit
2825

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	V	Naum et al., Automatic Functional Model Validation Between Spice and Verilog, 30 th Industry Application Conference, pages 1076-1083, October 1995.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.